



PATENT
Docket No. INTEL/17877

IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE

Applicant(s):

Ravishankar R. Iyer

U.S. Serial No.: 10/722,884

For: "Methods and Apparatus to
Process Cache Allocation Requests
Based on Priority"

Filed: November 26, 2003

Assignee: Intel Corporation

Group Art Unit: 2186

Examiner: Not Yet Assigned

) I hereby certify that this paper and the
documents referred to as enclosed
therewith are being deposited with the
United States Postal Service as first
class mail, postage prepaid, in an
envelope addressed to Commissioner
for Patents, P.O. Box 1450,
Alexandria, Virginia 22313-1450 on
this date:
) **February 7, 2005**
)
Mark G. Hanley
Attorney for Applicant(s)
Registration No. 44,736

STATUS LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter. Please note that it has been more than seven months since the last communication.

Respectfully submitted,

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February 7, 2005

By:

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